

**Amendment to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. – 4. (canceled)

5. (original) A data prefetch method for generating a prefetch instruction for indirect reference in correspondence with a cache line when values of an indirect reference array used as indices for a program of making indirect reference to an array increase or decrease in accordance with a predetermined rule, and a rate of increase or decrease is within a predetermined range, in a compiler of a programming language for generating an instruction to a microprocessor having a prefetch instruction~~The data prefetch method according to claim 1, wherein said increase or decrease in ~~the~~said values of the array of indirect reference used as said indices and the rate of increase or decrease ~~decrease~~, are determined in such a manner that source code of a loop including said indirect reference is presented to ~~the user~~a user and the user instructs a change amount of each of ~~the~~said indices.~~

6. – 7. (canceled)

8. (new) A data prefetch method in a compiler of a programming language for generating an instruction to a microprocessor having a prefetch instruction, and for a program of making indirect reference to an array, comprising the steps of:

setting values used as indices of said indirect reference array increase or decrease, and, deciding whether a rate of said increase or said decrease is within a predetermined range or not, in such a manner that source code of a loop including said indirect reference is presented to a user and the user instructs a change amount of each of the indices; and

generating said prefetch instruction for indirect reference in correspondence with a cache line.